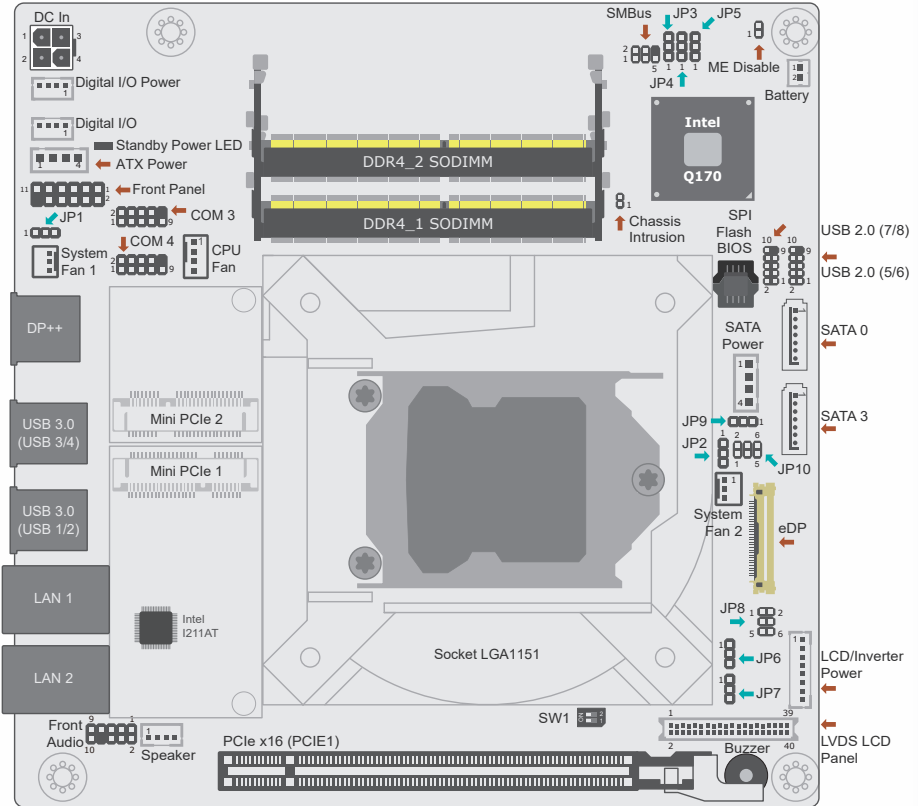


### Board Layout and Jumper Settings



#### Auto Power-on Select **JP1**

Via power button (default)	1-2 On
Via AC power	2-3 On

#### eDP BackLight Power Select **JP9**

+3.3V (default)	1-2 On
+5V	2-3 On

#### eDP Invert Power Select **JP2**

+12V (default)	1-2 On
+5V	2-3 On

#### eDP Panel Power Select **JP10**

+12V	1-2 On
+5V	3-4 On
+3.3V (default)	5-6 On

#### LVDS Backlight Power Select **JP6**

+3.3V (default)	1-2 On
+5V	2-3 On

#### LCD/Inverter Power Select **JP7**

+12V (default)	1-2 On
+5V	2-3 On

#### LVDS Panel Power Select **JP8**

+12V	1-2 On
+5V	3-4 On
+3.3V (default)	5-6 On

#### Clear CMOS Data **JP5**

Normal (default)	1-2 On
Clear CMOS Data	2-3 On

PCIE1 Signal Select	SW1
PCIe x16 (default)	1 On, 2 On
Two PCIe x8	1 Off, 2 On
One PCIe x8 + Two PCIe x4	1 Off, 2 Off

Mini PCIe Signal Select	JP4 (Mini PCIe 1)	JP3 (Mini PCIe 2)
Mini PCIe	1-2 On	1-2 On (default)
mSATA	3-4 On (default)	3-4 On

## Pin Assignments for I/O Connectors

### COM Ports (COM 3/4)

Pin	Assignment	Pin	Assignment
1	DCD	2	RD
3	TD	4	DTR
5	GND	6	DSR
7	RTS	8	CTS
9	RI		

### SMBus

Pin	Assignment	Pin	Assignment
1	3V3DU	2	GND
3	SMB_CLK	4	SMB_DATA
5	SMB_ALERT	6	---

### Chassis Intrusion

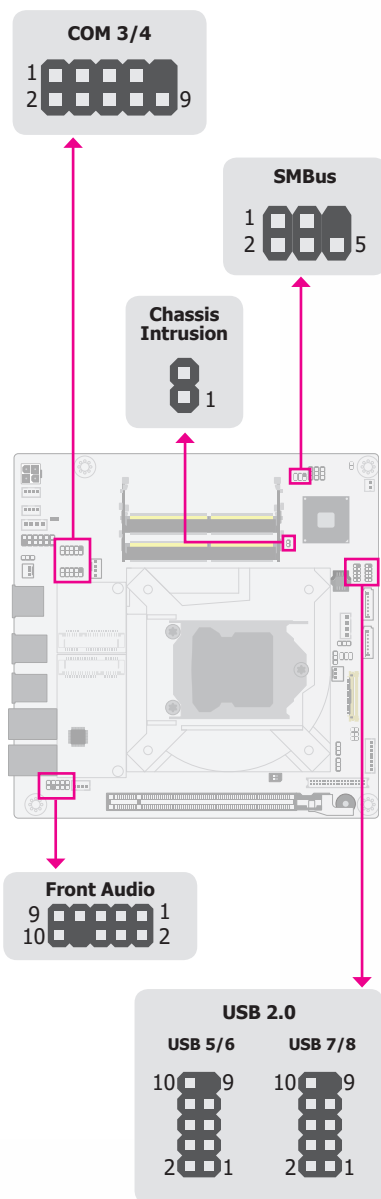
Pin	Function	Pin	Function
1	Signal	2	GND

### Front Audio

Pin	Assignment	Pin	Assignment
1	Mic-L	2	GND
3	Mic-R	4	N.C.
5	Line-R	6	Mic-JD
7	GND	8	---
9	Line-L	10	Line-JD

### USB 2.0 Headers (USB 5-8v)

Pin	Assignment	Pin	Assignment
1	VCC	2	VCC
3	DATA0-	4	DATA1-
5	DATA0+	6	DATA1+
7	GND	8	GND
9	---	10	NC



## Digital I/O Power

Pin	Assignment	Pin	Assignment
1	+12V	2	Ground
3	5VDU	4	+5V

## Digital I/O (DIO)

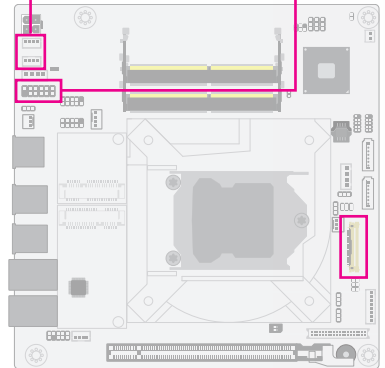
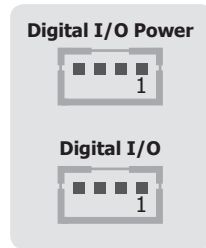
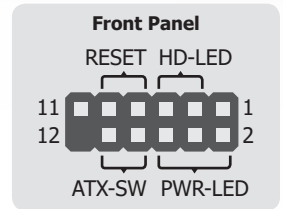
Pin	Function	Pin	Function
1	DIO0	2	DIO1
3	DIO2	4	DIO3

## Front Panel

	Pin	Assignment		Pin	Assignment
HD-LED	1	NC	PWR-LED	2	LED Power
	3	HDD Power		4	LED Power
	5	Signal		6	Signal
RESET	7	GND	ATX-SW	8	GND
	9	Signal		10	Signal
	11	NC		12	---

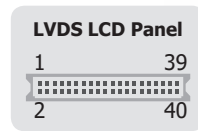
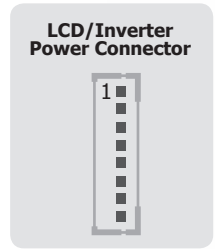
## eDP

	Pin	Assignment	15	eDP_AUX+		
	1	NA	16	eDP_AUX-		
	2	GND	17	GND		
	3	eDP_LANE3-	18	eDP Panel Power		
	4	eDP_LANE3+	19	eDP Panel Power	30	GND
	5	GND	20	eDP Panel Power	31	GND
	6	eDP_LANE2-	21	eDP Panel Power	32	eDP_Backlight On/Off Control
	7	eDP_LANE2+	22	NA	33	eDP Inverter Brightness Voltage Control
	8	GND	23	GND	34	NA
	9	eDP_LANE1-	24	GND	35	NA
	10	eDP_LANE1+	25	GND	36	Inverter Power
	11	GND	26	GND	37	Inverter Power
	12	eDP_LANE0-	27	eDP_HPDI	38	Inverter Power
	13	eDP_LANE0+	28	GND	39	Inverter Power
	14	GND	29	GND	40	NA



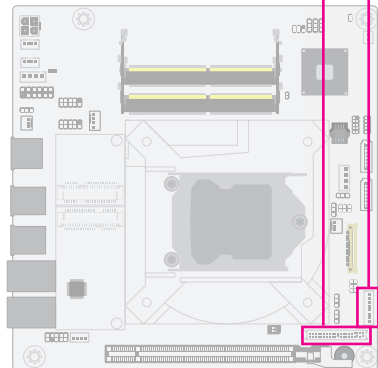
## LVDS LCD Panel Connector

Pin	Assignment	Pin	Assignment
1	GND	2	GND
3	LVDS_Out3+ (Odd_3+)	4	LVDS_Out7+ (Even_3+)
5	LVDS_Out3- (Odd_3-)	6	LVDS_Out7- (Even_3-)
7	GND	8	GND
9	LVDS_Out2+ (Odd_2+)	10	LVDS_Out6+ (Even_2+)
11	LVDS_Out2- (Odd_2-)	12	LVDS_Out6- (Even_2-)
13	GND	14	GND
15	LVDS_Out1+ (Odd_1+)	16	LVDS_Out5+ (Even_1+)
17	LVDS_Out1- (Odd_1-)	18	LVDS_Out5- (Even_1-)
19	GND	20	GND
21	LVDS_Out0+ (Odd_0+)	22	LVDS_Out4+(Even_0+)
23	LVDS_Out0- (Odd_0-)	24	LVDS_Out4- (Even_0-)
25	GND	26	GND
27	LVDS_CLK1+ (Odd_CLK+)	28	LVDS_CLK2+ (Even_CLK+)
29	LVDS_CLK1- (Odd_CLK-)	30	LVDS_CLK2- (Even_CLK-)
31	GND	32	GND
33	LVDS_DDC_CLK	34	NC
35	LVDS_DDC_DATA	36	+3.3V
37	Panel Power	38	Panel Power
39	Panel Power	40	Panel Power



## LCD/Inverter Power Connector

Pin	Assignment
1	GND
2	GND
3	Panel Inverter Brightness Voltage Control
4	Panel Power
5	+3.3V
6	Panel Backlight On/Off Control
7	12V (default)/5V
8	12V (default)/5V



DFI reserves the right to change the specifications at any time prior to the product's release. For the latest information about the product, please visit the document download page at [go.dfi.com/SD106-Q170](http://go.dfi.com/SD106-Q170) or via the QR code to the right.

